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Total Number of Pages in This Submission

Application Number 10/820,336

Filing Date April 8, 2004

First Named Inventor Robert O. Conn

Art Unit 2819

Examiner Name Anh Q. Tran

Attorney Docket Number X-1571 US

ENCLOSURES (Check all that apply)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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Date October 25, 2005

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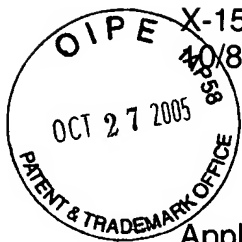
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X-1571 US
10/820,336

PATENT
Conf. No.: 8549

IN THE UNITED STATES PATENT OFFICE

Applicant: Robert O. Conn
Assignee: Xilinx, Inc.
Title: Coaxial Clock Tree for Programmable Logic Devices

Serial No.:	10/820,336	File Date:	04-08-04
Examiner:	Anh Q. Tran	Art Unit:	2819
Docket No.:	X-1571 US	Conf. No.:	8549

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RESPONSE TO NOTICE OF DRAWING INCONSISTENCY
WITH SPECIFICATION

Dear Sir:

In response to the Notice of drawing inconsistency with specification mailed from the Patent Office on October 6, 2005, please replace/substitute the following paragraph as indicated.